

**WRITTEN OFFICE ACTION  
OF THE INTERNATIONAL  
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**International File No.  
PCT/AT2004/000240**

1) Reference is made to the following documents:

D(1): PATENT ABSTRACTS OF JAPAN Vol. 2000, No. 25, April 12, 2001  
(04/12/2001)  
& JP 2001 218450 A (RICOH CO LTD), August 10, 2001 (08/10/2001)

D(2): US-A-4 229 669

2) The present application does not meet the requirements of Article 33(1) PCT, because the subject of claim 1 is not novel in terms of Article 33(2) PCT.

Document D(1), Figure 3, shows a circuit for producing galvanically separated synchronization impulses (VO) from an AC mains, whereby the line voltage rectified by means of a half-wave rectifier (D2), lies on a voltage divider (R2, R3, R4) for the switching input of a semiconductor switch (Q1), the emitting diode (PC1-D) of an optocoupler (PC1) lies in the working branch of the switch (Q1), whereby the working branch has, in series with the emitting diode (PC1-D), a drop resistor (R1), via which a storage capacitor (C1) can be periodically charged and can be discharged via the emitting diode (PC1-D), and a transistor (Q2), which is fed by a voltage source (VB) that is galvanically separated from the line voltage and in whose load branch the essentially rectangular synchronization impulses (VO) are available, is arranged downstream of the receiving element (PC1-Q) of the optocoupler (PC1).

3) The additional features of the dependent claims 2-8 are either known from the disclosed state of the art or are rendered obvious by same, cf. concerning

- claim 2: D(1), Figure 3, transistor Q1
- claim 3: D(1), Figure 3, Zener diode ZD1
- claim 4: D(1), Figure 3, current-limiting resistor R5
- claim 5: D(2), Figure 2, resistor R5
- claim 6: D(1), Figure 3, filter capacitor C2
- claims 7, 8: obvious modification of the basic circuit, known from D(1), based on purely craft-related activity